

EAST Search History

Interference Search

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9453	access with (determin\$3 compar\$3) with (key parameter predetermined)	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:51
L2	71534	(blocking security) near3 (circuit device control controller)	US-PGPUB; USPAT; JPO; IBM_TDB	OR	ON	2007/07/10 09:52
L3	206	((match matching) same 1).clm.	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:54
L4	189	((match matching) with 1).clm.	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:56
L5	23538	(blocking security) adj (circuit device control controller)	US-PGPUB; USPAT; JPO; IBM_TDB	OR	ON	2007/07/10 09:54
L6	6156	711/163 711/112	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:55
L7	12	3 and 6	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:55
L8	10	4 and 6	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:56
L9	40	(1 with 2).clm.	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:56
L10	96988	(access operation) near3 (key parameter authentication)	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:56
L11	455	6 and 10	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:56
L12	202	(match matching) and 11	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:57
L13	2533	virus near3 (definition table file)	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:57
L14	0	1 same 13	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:57

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L15	19	1 and 13	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:57
L16	163	2 and 13	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:57
L17	8	6 and 16	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:58
L18	35	5 and 13	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:58
L19	267	((command read write) same (match match\$3)) same 2	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 09:59
L20	124	((command read write) with (match match\$3)) same 2	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 10:00
L21	7	((command read write) with (match match\$3)) same 13	US-PGPUB; USPAT; JPO; IBM_TDB	OR	OFF	2007/07/10 10:00

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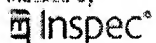
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deny <or> denying <or> prohibit <or>
prohibiting <sentence> access <paragraph>
security

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<input type="text" value="deny <or> denying <or> prohibit <or> prohibiting <sentence> access <paragraph> security <paragraph> memory <or> storage"/>	<input type="button" value="v"/>
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45. **Trade-offs in protecting storage: a meta-data comparison of cryptograph backup/versioning, immutable/tamper-proof, and redundant storage solu**
 Tucek, J.; Stanton, P.; Haubert, E.; Hasan, R.; Brumbaugh, L.; Yurcik, W.;
[Mass Storage Systems and Technologies, 2005. Proceedings. 22nd IEEE / 13](#)
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46. **SPEK: a storage performance evaluation kernel module for block level st**
 Zhang, M.; Yang, Q.; He, X.;
[Modeling, Analysis and Simulation of Computer Telecommunications Systems](#)
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47. **A MICRO high-temperature superconductor-magnet flywheels with dual f**
 energy storage and attitude control
 Lee, E.;
[Energy Conversion Engineering Conference, 2002. IECEC '02. 2002 37th Inte](#)
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[Microelectromechanical Systems Conference, 2001](#)
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 Wilkes, J.;
[System Sciences, 1997. Proceedings of the Thirtieth Hawaii International Con](#)
[Volume 1, 7-10 Jan. 1997 Page\(s\):724 vol.1](#)
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 T-Raissi, A.; Banerjee, A.; Sheinkopf, K.;
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 Volume 4, 11-16 Aug. 1996 Page(s):2280 - 2285 vol.4
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protect <or> protecting <paragraph> memory
<paragraph> virus <or> hacker <or> attack

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31. **Asynchronous zero-copy communication for synchronous sockets in the protocol (SDP) over InfiniBand**

Balaji, P.; Bhagvat, S.; Jin, H.-W.; Panda, D.K.;
[Parallel and Distributed Processing Symposium, 2006. IPDPS 2006. 20th Inter](#)
25-29 April 2006 Page(s):8 pp.

Digital Object Identifier 10.1109/IPDPS.2006.1639560

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32. **A novel hierarchical matching algorithm for intrusion detection systems**

Tzu-Fang Sheu; Nen-Fu Huang; Hsiao-Ping Lee;
[Global Telecommunications Conference, 2005. GLOBECOM '05. IEEE](#)
Volume 3, 28 Nov.-2 Dec. 2005 Page(s):5 pp.

Digital Object Identifier 10.1109/GLOCOM.2005.1577938

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33. **InfoShield: a security architecture for protecting information usage in me**

Shi, W.; Fryman, J.B.; Gu, G.; Lee, H.-H.S.; Zhang, Y.; Yang, J.;
[High-Performance Computer Architecture, 2006. The Twelfth International Sy](#)
11-15 Feb. 2006 Page(s):222 - 231

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34. **Improving scratch-pad memory reliability through compiler-guided data l**

Li, F.; Chen, G.; Kandemir, M.; Kolcu, I.;
[Computer-Aided Design, 2005. ICCAD-2005. IEEE/ACM International Confere](#)
6-10 Nov. 2005 Page(s):1002 - 1005

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35. **The Starfire SMP Interconnect**

Charlesworth, A.; Aneshansley, N.; Haakmeester, M.; Drogichen, D.; Gilbert, C
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[Supercomputing, ACM/IEEE 1997 Conference](#)

15-21 Nov. 1997 Page(s):37 - 37

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36. **Minos: Control Data Attack Prevention Orthogonal to Memory Model**

Crandall, J.R.; Chong, F.T.;
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37. **Defeating memory corruption attacks via pointer taintedness detection**

Chen, S.; Xu, J.; Nakka, N.; Kalbarczyk, Z.; Iyer, R.K.;
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